

Range Avoidance for Constant-Depth Circuits: Hardness and Algorithms

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Abstract

Range Avoidance (AVOID) is a total search problem where, given a Boolean circuit $C: \{0,1\}^n \to \{0,1\}^m, m > n$, the task is to find a $y \in \{0,1\}^m$ outside the range of C. For an integer $k \ge 2$, NC_k^0 -AVOID is a special case of AVOID where each output bit of C depends on at most k input bits. While there is a very natural randomized algorithm for AVOID, a deterministic algorithm for the problem would have many interesting consequences. Ren, Santhanam, and Wang (FOCS 2022) and Guruswami, Lyu, and Wang (RANDOM 2022) proved that explicit constructions of functions of high formula complexity, rigid matrices, and optimal linear codes, reduce to NC_4^0 -AVOID, thus establishing conditional hardness of the NC_4^0 -AVOID problem. On the other hand, NC_2^0 -AVOID admits polynomial-time algorithms, leaving the question about the complexity of NC_3^0 -AVOID open.

We give the first reduction of an explicit construction question to NC₃⁰-Avoid. Specifically, we prove that a polynomial-time algorithm (with an NP oracle) for NC₃⁰-Avoid for the case of $m = n + n^{2/3}$ would imply an explicit construction of a rigid matrix, and, thus, a super-linear lower bound on the size of log-depth circuits.

We also give deterministic polynomial-time algorithms for all NC⁰_k-Avoid problems for $m \ge n^{k-1}/\log(n)$. Prior work required an NP oracle, and required larger stretch, $m \ge n^{k-1}$.

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1 Introduction

The Range Avoidance (AVOID) problem is: given a Boolean circuit $C: \{0,1\}^n \to \{0,1\}^m$ for some stretch m > n, find an element $y \in \{0,1\}^m$ outside the range of C. By the pigeonhole principle, such a y always exists. This problem was first introduced by Kleinberg, Korten, Mitropolsky, and Papadimitriou [KKMP21] as a complete problem for the class APEPP (Abundant Polynomial Empty Pigeonhole Principle). Informally, APEPP contains total search problems where the existence of a solution follows via the union bound (such as Shannon's classical proof that most functions require circuits of exponential size).

Korten [Kor21] proved that a deterministic algorithm for AVOID would imply explicit constructions of objects that are central to the field of computational complexity, and would resolve several long-standing open problems. Such objects include functions of high circuit complexity, rigid matrices, pseudorandom generators, and Ramsey graphs. The key idea is that there is a succinct way to encode all "easy" objects (such as descriptions of functions of low circuit complexity) in the input space of a small circuit that acts as a decoder. Then a solution to the AVOID problem yields a "hard" object (such as a function of high circuit complexity), implying an explicit construction. In fact, the aforementioned works [KKMP21, Kor21] showed that even a deterministic algorithm *with an* NP *oracle* solving AVOID in polynomial time would lead to breakthrough results in complexity theory.

The only known deterministic algorithm for AVOID is the trivial brute force algorithm running in time $2^n \cdot \text{poly}(n, |\mathsf{C}|)$.¹ No better algorithms are known for AVOID even if the algorithm is allowed to use an NP oracle. On the other hand, using both an NP oracle and randomness, one can solve AVOID in polynomial time: Pick a random string $y \in \{0, 1\}^m$, and simply check if $y \in \text{Range}(\mathsf{C})$ using the NP oracle. This shows that AVOID $\in \mathsf{FZPP}^{\mathsf{NP}}$, and, using the standard trick of simulating randomness by non-uniformity, AVOID $\in \mathsf{FP}^{\mathsf{NP}}/\mathsf{Poly}$.² Interestingly, for the class of polynomialtime algorithms with an NP oracle, the AVOID problem is equally hard for all values of stretch $n+1 \leq m \leq \operatorname{poly}(n)$ [KKMP21].

For a class of circuits C, the C-AVOID problem is a special case of AVOID where each output is computed by a circuit from C. Recent works by Ren, Santhanam, and Wang [RSW22] and Guruswami, Lyu, and Wang [GLW22] proved that efficient algorithms for C-AVOID even for certain simple circuit classes C would be sufficient for getting various explicit constructions. Later, Chen, Huang, Li, and Ren [CHLR23] re-derived the best known lower bounds against ACC⁰ circuits from an efficient algorithm for a certain C-AVOID problem. While this suggests that designing efficient algorithms for AVOID problems is a promising approach to various explicit construction questions, the work of Ilango, Li, and Williams [ILW23] proves barriers for designing polynomial time algorithms under certain cryptographic assumptions.

Let NC^1 denote the class of Boolean fan-in-2 circuits of depth $O(\log(n))$, and NC_k^0 denote the class of Boolean functions where each output depends on at most k inputs for a constant k. [RSW22] used perfect encodings of [IK00, IK02, AIK06] to reduce NC^1 -AVOID to NC_4^0 -AVOID in polynomial time. Consequently, [GLW22] reduced most of the aforementioned explicit construc-

¹This trivial algorithm is (conditionally) tight for a related problem studied in [KKMP21], where the range of C has size much smaller than 2^{n+1} , and is given by a circuit computing a function from [N] to [M]. [KKMP21] gives a deterministic reduction from SAT on n variables to AVOID for a circuit C: $[2^n] \rightarrow [2^n + 2^{o(n)}]$ running in subexponential time. Thus, under the Exponential Time Hypothesis [IPZ98, IP99], this problem does not admit deterministic (and randomized) algorithms running in time $2^{o(n)}$.

²Here, the complexity classes $\mathsf{FP}, \mathsf{FE}, \mathsf{FZPP}$ are simply the functional analogs of the decision classes $\mathsf{P}, \mathsf{E}, \mathsf{ZPP}$.

tions in [Kor21] (and several new ones!) to NC^1 -AVOID, and, thus, to NC_4^0 -AVOID. In particular, polynomial-time *deterministic* algorithms (even with an NP oracle) for NC_4^0 -AVOID would now imply breakthrough results in complexity theory.

[GLW22] gave a polynomial-time algorithm solving NC₂⁰-AVOID for any stretch $m \ge n + 1$. As mentioned above, NC₄⁰-AVOID might be hard to solve efficiently. This leaves the question about the complexity of NC₃⁰-AVOID open.

Open Problem 1 ([GLW22]). Can we reduce explicit construction problems to solving NC_3^0 -AVOID? Or can we solve NC_3^0 -AVOID in polynomial time?

Unlike the case of the general AVOID problem, NC_k^0 -AVOID may be much easier for large values of the stretch m. Indeed, on one hand, NC_k^0 -AVOID for small stretch m = n + o(n) is capable of encoding hard explicit construction problems [RSW22, GLW22]. On the other hand, NC_k^0 -AVOID for $m = \Omega(n^k)$ is easily solvable in polynomial time: since the number of distinct functions depending on at most k out of n inputs is $O(n^k)$, every such instance of the problem must have two outputs computing identical functions. Assigning different values to these outputs solves NC_k^0 -AVOID.

[GLW22] presented an algorithm solving NC_k^0 -AVOID for stretch $m \ge \Omega(n^{k-1})$ in polynomial time with an NP oracle.³ This improvement on the trivial algorithm suggests a natural question of whether one can solve NC_k^0 -AVOID for even smaller values of stretch m.

Open Problem 2. Design a polynomial-time algorithm (with an NP oracle) solving NC_k^0 -AVOID with n inputs and stretch $m = o(n^{k-1})$ for $k \ge 3$.

1.1 Our Results

The classical result of Shannon [Sha49] shows that most Boolean functions of n variables require Boolean circuits of exponential size. Despite that, the best known lower bound on the size of a circuit (or even a circuit of logarithmic depth, i.e., NC¹) for a function in P (or even E^{NP}) is 3.1n - o(n)proven by Li and Yang [LY22]. A central problem in circuit complexity is to prove a super-linear lower bound on the number of gates of NC¹ circuits computing an explicit function [Val77, AB09, Frontier 3].

Similarly, for the class of *linear* NC^1 circuits— NC^1 circuits where each gate computes the XOR (or its negation) of its two inputs—no super-linear lower bound on the complexity of an explicit linear map $M \in \mathbb{F}_2^{n \times n}$ is known. The best lower bound against linear circuits is 3n - o(n) proven by Chashkin [Cha94].

In our first result (Theorem 3.2), we answer Open Problem 1 by showing that a polynomialtime algorithm for NC_3^0 -Avoid would imply an explicit construction of a map requiring linear NC^1 circuits of super-linear size (thus, demonstrating the hardness of NC_3^0 -Avoid).

Theorem 1. An FP (resp. $\mathsf{FP}^{\mathsf{NP}}$) algorithm for NC_3^0 -AVOID with stretch $m = n + O(n^{2/3})$ implies an explicit construction of a linear map in FP (resp. $\mathsf{FP}^{\mathsf{NP}}$) that cannot be computed by linear NC^1 circuits of size $o(n \log \log(n))$.

Our proof of Theorem 1 first reduces an explicit construction of a rigid matrix to NC₃⁰-Avoid (Theorem 3.2). A matrix $M \in \mathbb{F}_2^{n \times n}$ is called (r, s)-rigid if it cannot be written as a sum M =

³The algorithm of [GLW22] does not use the full power of $\mathsf{FP}^{\mathsf{NP}}$: it outputs a hitting set $H \subseteq \{0, 1\}^m$ such that for every NC^0_k function C, at least one point $y \in H$ is outside the range of C. Only then the algorithm looks at the input function and finds a solution $y \in H$ using the NP oracle.

L + S of a rank-*r* matrix *L* and a matrix *S* with at most *s* non-zeros per row. In a seminal work, Valiant [Val77] introduced an approach for proving super-linear lower bounds on the size of linear NC¹ circuits via matrix rigidity. Valiant proved that an $(\varepsilon n, n^{\varepsilon})$ -rigid matrix $M \in \mathbb{F}_2^{n \times n}$ for any constant $\varepsilon > 0$ requires linear NC¹ circuits of size $\Omega(n \log \log(n))$. Theorem 1 now follows straightforwardly as a corollary of Theorem 3.2.

The best known constructions of rigid matrices do not yet achieve the parameters sufficient for Valiant's circuit lower bound. [Fri93, PR94, SSS97] construct an $\left(r, \Omega\left(\frac{n}{r}\log\left(\frac{n}{r}\right)\right)\right)$ -rigid matrix in polynomial time, [GT16] gives an $\left(r, \Omega\left(\frac{n^2}{r^2\log(n)}\right)\right)$ -rigid matrix in time $2^{O(n)}$ for $r \ge \sqrt{n}$, and [AC19, BHPT20] give $\left(2^{\varepsilon\log(n)/\log\log(n)}, \Omega(n)\right)$ -rigid matrices in polynomial time with an NP oracle. However, even an FP^{NP} algorithm for NC₃⁰-AvOID with stretch $m = n + n^{12/17-\varepsilon}$ for any constant $\varepsilon > 0$ would already improve on these known constructions of rigid matrices.

In fact, we reduce the problem of constructing explicit rigid matrices to a problem that we call degree-2-AVOID, where each output computes a degree-2 polynomial of the inputs. Following Ren, Santhanam, and Wang's approach [RSW22], this problem can be reduced to NC_3^0 -AVOID using the perfect encoding scheme of Applebaum, Ishai, and Kushilevitz [AIK06].⁴

On the algorithmic side, we make partial progress towards resolving Open Problem 2. We first give a simple deterministic polynomial-time algorithm for NC_3^0 -Avoid for stretch $m \ge {n \choose 2}/3 + 2n$ (presented in Appendix A). This algorithm already improves on the best known algorithm for NC_3^0 , as it does not use an NP oracle. Then, in Theorem 4.5 we extend this algorithm to solve NC_k^0 -Avoid for all constant k. Recall that the current best algorithms for this problem solve the case where $m \ge \Omega(n^{k-1})$ in polynomial time using an NP oracle [GLW22]. We improve this result in two directions: our algorithm does not use an NP oracle, and it works in polynomial time for stretch $m \ge n^{k-1}/\log(n)$.

Theorem 2. There is a deterministic polynomial-time algorithm that solves the NC_k^0 -AVOID problem with n inputs and stretch m for every $k \ge 3$ and $m \ge n^{k-1}/\log(n)$.

1.2 **Proof Overview**

Hardness of NC⁰₃-Avoid. Valiant [Val77] proved that linear NC¹ circuits with a linear number of gates can only compute non-rigid linear maps $M \in \mathbb{F}_2^{n \times n}$, i.e., maps M that can be written as a sum M = Q + S, where rank $(Q) \leq \varepsilon n$ and each row of S has at most n^{δ} ones in it. For the rest of the section, our non-rigid matrices can be written as the sum of a matrix with rank $\leq n/10$ and a matrix with row sparsity at most $n^{0.1}$. Therefore, constructing a rigid matrix would imply a super-linear lower bound on the size of linear NC¹ circuits computing it.

To reduce an explicit construction of an $n \times n$ rigid matrix to solving an instance of NC₃⁰-AVOID, we design an NC₃⁰ function $f: \{0,1\}^{p(n)} \to \{0,1\}^{n^2}$, for some polynomial $p(n) < n^2$, such that for every non-rigid matrix $M \in \{0,1\}^{n \times n}$, there exists $x \in \{0,1\}^{p(n)}$ satisfying f(x) = M. Now, any solution $M' \in \{0,1\}^{n \times n}$ to the NC₃⁰-AVOID problem for the function f must be a rigid matrix.

Before constructing such an NC_3^0 function f, we first design a function $g: \mathbb{F}_2^{n^2/2} \to \mathbb{F}_2^{n^2}$, where each output bit of g is a degree-2 polynomial of the inputs, and the range of g contains all non-rigid matrices. A solution to the degree-2-AVOID problem for the function g would give us a rigid matrix. Following [RSW22], we can then apply a perfect encoding scheme [IK00, IK02, AIK06] to g, and

⁴Ren, Santhanam, and Wang use the following definition of perfect encodings: A function \hat{f} is a perfect encoding of a function f if there exists a polynomial time algorithm Dec such that for all x, y: $Dec(y) = f(x) \iff \exists r, y = \hat{f}(x, r)$.

obtain an NC_3^0 function f, as required (see Lemma 2.7). Effectively, this reduces solving AVOID on g to solving AVOID on f.

Now we construct a degree-2 function $g: \mathbb{F}_2^{n^2/2} \to \mathbb{F}_2^{n^2}$ whose inputs encode all non-rigid matrices, i.e., for every non-rigid matrix M, there is an $x \in \{0,1\}^{n^2/2}$ such that f(x) = M. A non-rigid matrix M can be written as M = LR + S, where $L, R^T \in \mathbb{F}_2^{n \times n/10}$, and each row of S contains at most $n^{0.1}$ ones. The first $n^2/5$ inputs of the function g will correspond to the elements of L and R. Note that every entry of LR is a degree-2 function of the entries of L and R since it just computes the inner product of a row in L and a column in R. Now, for each $n^{0.1}$ -sparse row of S, we show how to encode it using $n^{0.6}$ inputs and a degree 2 function. Repeating this procedure for each row of S will finish the proof.

We interpret the $n^{0.1}$ -sparse row with n entries as a $\sqrt{n} \times \sqrt{n}$ matrix A. Since A has at most $n^{0.1}$ non-zero entries, rank $(A) \leq n^{0.1}$. It can be written as a product A = BC where $B, C^T \in \mathbb{F}_2^{\sqrt{n} \times n^{0.1}}$. Therefore, there is a degree-2 function h that takes as input B, C of size $2n^{0.6}$ and outputs the sparse matrix A.

The presented encoding of s-sparse vectors in \mathbb{F}_2^n is only non-trivial for $s < \sqrt{n}$ (as otherwise the number of inputs of h exceeds the number of outputs). As a result, we cannot encode the entire matrix S as an $n^{1.1}$ -sparse vector in $\mathbb{F}_2^{n^2}$. However, for Valiant's approach of proving circuit lower bounds, we can assume that S is $n^{0.1}$ -row sparse.⁵ Thus, we can separately encode each row of S using only $O(n^{0.6})$ inputs to obtain an encoding of S in $O(n^{1.6})$ bits. In Lemma 3.1, we will demonstrate how to accommodate slightly better sparsity parameter when we are allowed higher (but still constant) degree d for the encoding function.

Simple algorithm for NC₃⁰-Avoid. We start with a short description of a simple deterministic polynomial-time algorithm for NC₃⁰-Avoid for stretch $m \ge \binom{n}{2}/3 + 2n$ (presented in Appendix A). This algorithm already improves on the best known algorithm for NC₃⁰-Avoid, as our algorithm does not use an NP oracle.

If we had a #SAT oracle, then we could solve NC_3^0 -Avoid even for stretch m = n + 1. Our algorithm would iteratively find constant assignments to each of the first n outputs to minimize the number of inputs that map to the current (partial) output assignments. Throughout our exposition, we say such inputs are *consistent with the (partial) output assignments*. Before we describe our algorithm, it is important to note that we always fix circuit outputs to constant assignments. At each iteration, we would use the #SAT oracle to find the output assignment that reduces the size of the input set by at least half. After fixing the first n outputs, we still have at least $m - n \ge 1$ unassigned outputs, and only one input point $x \in \mathbb{F}_2^n$ that is consistent with the previously assigned output bits. This allows us to find an assignment of the (n + 1)-th output bit such that the string specified by the output bits lies outside the range of the circuit.

Unfortunately, solving #SAT (even approximately) is hard for this class of multi-output circuits. In the absence of an efficient #SAT algorithm, our algorithm maintains an affine subspace S that contains all inputs from \mathbb{F}_2^n that are consistent with the current partial assignment (S may also contain inputs that are not consistent with the current partial assignment). We carefully set output values so that at each iteration, we reduce the dimension of S by at least one. This way, after n + 1steps we will find a solution to the NC₃⁰-Avoid problem. However, our algorithm can only work

⁵Alternatively, one can argue by Markov's inequality that if a matrix M cannot be written as a sum of rank-r and $n^{0.1}$ -row sparse matrices, then M also cannot be written as a sum of rank-2r and $rn^{0.1}$ -globally sparse matrices.

when the stretch is $m \ge \Omega(n^2)$.

Without loss of generality, we assume that each output reads exactly three input bits. At each iteration the number of currently unassigned outputs is $> \binom{n}{2}/3$. This allows us to find a pair of outputs y_1 and y_2 that share a pair of input variables.⁶ Say, $y_1 = f_1(x_1, x_2, x_3)$ and $y_2 = f_2(x_2, x_3, x_4)$. We will find a constant assignment to y_1 and y_2 that reduces the dimension of the affine subspace S.

Note that there are 16 assignments to (x_1, x_2, x_3, x_4) and four different values of (y_1, y_2) . Therefore, there is a way to assign $y_1 = c_1, y_2 = c_2$ such that at most four points (x_1, x_2, x_3, x_4) map to these values of the outputs. Note that there always exists a hyperplane H containing any four points in \mathbb{F}_2^4 . Let \mathcal{H} be the affine subspace obtained by extending H to all n inputs. Then, $S \cap \mathcal{H}$ gives us an affine subspace containing all inputs consistent with the assignment $y_1 = c_1, y_2 = c_2$.

If $S \not\subseteq \mathcal{H}$, then we have an assignment of two outputs that reduces the dimension of our affine subspace as $\dim(S \cap \mathcal{H}) < \dim(S)$. Otherwise, if $S \subseteq \mathcal{H}$, then instead of considering all 16 assignments to the inputs (x_1, x_2, x_3, x_4) , we can restrict our attention to at most 8 such assignments that belong to H, which makes the problem only easier (as we show in Theorem A.1).

Better algorithm for NC_k^0 -Avoid. The main bottleneck of this simple algorithm is that it maintains an affine subspace that must contain all consistent inputs. While affine subspaces are easy to work with, they are not expressive enough to accurately describe all inputs that are consistent with an arbitrary partial assignment. In order to improve the previous algorithm, we will maintain a more expressive structure than an affine subspace—a union of affine subspaces. Below we sketch our approach to solving NC_3^0 -Avoid with stretch $m \ge \Omega(n^2/\log(n))$. Theorem 4.5 generalizes this to solving NC_k^0 -Avoid problems with stretch $m \ge \Omega(n^{k-1}/\log(n))$ for all values of k.

Again, without loss of generality, we assume that each output depends on exactly three inputs. Consider a bipartite graph, where the left vertices correspond to n inputs, the right vertices correspond to m outputs, and an input-output pair (x_i, y_j) is connected by an edge if the output y_j depends on the input x_i . First we select $t = 3n^2/m$ highest-degree inputs $I = \{x_1, \ldots, x_t\}$. Their neighborhood must contain at least 3n distinct outputs $O = \{y_1, \ldots, y_{3n}\}$.⁷ Let C be the sub-circuit defined on outputs from O and their corresponding inputs. Now, we will find a $y \in \mathbb{F}_2^{3n}$ outside Range(C).

First, consider all 2^t assignments to the inputs in $I = \{x_1, \ldots, x_t\}$, resulting in circuits C_1, \ldots, C_{2^t} . Since every output in O is connected to at least one input from I, fixing an assignment to the inputs I reduces each C_i to an NC_2^0 circuit. In a way, we have reduced NC_3^0 -Avoid to an OR of 2^t instances of NC_2^0 -Avoid: we need to find a $y \in \{0,1\}^{3n}$ outside the ranges of all the C_i 's. For each circuit C_i , we will maintain an affine subspace S_i containing all inputs consistent with the current partial assignment of the outputs.

Our algorithm works by iteratively fixing the output bits from $\{y_1, \ldots, y_{3n}\}$ such that at each step the total number of points in the (disjoint) union of the affine subspaces S_i is reduced by a constant factor, eventually making all the subspaces empty. We observe (in Lemma 4.3) that for any affine subspace S_i , one of the assignments $y_i = 0$ or $y_i = 1$ always reduces the dimension of S_i by one. Therefore, by picking the "best" assignment $y_i = c$ across all the subspaces S_i , we can

⁶Each output sees 3 pairs of input bits, giving a total of $3m > \binom{n}{2}$ pairs. By the pigeonhole principle, at least one pair of inputs appears in two outputs.

⁷Since the number of outputs is m, and each output has degree 3, the number of edges in the graph is 3m, and the average degree of an input is 3m/n. The t highest-degree inputs then have total degree at least 3mt/n, and must be connected to at least mt/n = 3n distinct outputs.

reduce the size of the union of such affine subspaces by a constant factor of 4/3. Repeating this procedure for $\log_{4/3}(2^n) + 1 < 3n$ steps finishes the proof.

1.3 Open Problems

Our work motivates several natural questions about the complexity of NC_3^0 -Avoid and degree-2-Avoid. We reduce explicit constructions of rigid matrices to solving degree-2-Avoid, and then NC_3^0 -Avoid, with appropriate stretch.

Open Problem 3. Can other explicit construction questions be reduced to NC_3^0 -Avoid or degree-2-Avoid?

Particularly, we suspect that the construction of linear and list-decodable codes with optimal parameters [GLW22] might be good candidates for these reductions.

Using the encoding of [AIK06] in the reduction from degree-2-AVOID to NC₃⁰-AVOID almost always decreases the required stretch to m = n + o(n) (as highlighted in Section 3). It would also be interesting to find a more efficient encoding or reduction from degree-2-AVOID to NC₃⁰-AVOID. This could potentially increase the stretch for NC₃⁰-AVOID required to obtain explicit constructions thereby making the problem easier.

Open Problem 4. Can we construct a more efficient reduction from degree-2-Avoid to NC_0^3 -Avoid?

We believe degree-2-AVOID might be of independent interest since it allows for a larger stretch. For example, for improved constructions of rigid matrices, it suffices to solve degree-2-AVOID for super-linear stretch $m \ge n^{12/11-\varepsilon}$ for a constant $\varepsilon > 0$. In fact, degree-2-AVOID is easy to solve when the stretch is $m \ge n^2$. Note that there are at most $\binom{n}{2}$ unique degree-2 monomials on nvariables. If $m \ge n^2$, then we can replace each unique monomial with a new variable. As a result, we will have m linear functions in < m variables. We can solve AVOID on this linear function instance by a dimension reduction strategy similar to the one outlined in the previous section.

Open Problem 5. Are there algorithmic techniques to solve degree-2-AvOID that do not use a reduction to NC_3^0 -AvOID?

For the NC₃⁰-AVOID problem, our algorithm runs in deterministic time $2^{O(n^2/m)}$ for any stretch $m \ge n+1$. In particular, this recovers the exponential-time brute force algorithm for the hardest case of m = n + 1. It would be interesting to obtain matching conditional lower bounds for deterministic algorithms for NC₃⁰-AVOID.

Open Problem 6. Is there a conditional lower bound of $2^{\Omega(n^2/m)}$ on the complexity of deterministic algorithms without an NP oracle for NC₃⁰-Avoid?

Finally, it is natural to ask if algorithms with NP oracles can solve NC_3^0 -Avoid more efficiently.

Open Problem 7. Do there exist polynomial-time algorithms with NP oracles that solve NC₃⁰-AVOID for stretch $m = o(n^2/\log(n))$?

1.4 Structure

The rest of the paper is organized as follows. In Section 2, we give all necessary background material, including a reduction from degree-*d*-AVOID to NC_{d+1}^0 -AVOID in Section 2.3. In Section 3, we reduce the problem of constructing explicit rigid matrices to NC_3^0 -AVOID. In Section 4, we give deterministic algorithms solving NC_k^0 -AVOID in polynomial time for stretch $m \ge n^{k-1}/\log(n)$. Finally, Appendix A contains an alternative deterministic polynomial-time algorithm for NC_3^0 for the case where stretch $m \ge \Omega(n^2)$.

2 Preliminaries

For every $a \in \mathbb{F}_2^n$ and subspace L of \mathbb{F}_2^n , we can define an affine subspace $\mathcal{A} \subseteq \mathbb{F}_2^n$ where $\mathcal{A} = \{a + v \mid v \in L\}$. The dimension of the affine subspace dim (\mathcal{A}) is the same as the dimension of the linear subspace L that defines it. Equivalently, the set of points that lie on a specified set of hyperplanes over \mathbb{F}_2^n also characterize an affine subspace of \mathbb{F}_2^n . The hyperplanes can be written as a system of linear equations Ax = b, and the dimension of the corresponding affine subspace \mathcal{A} can be calculated as dim $(\mathcal{A}) = n - \operatorname{rank}(\mathcal{A})$.

The circuits and algorithms in this paper generally work over the boolean hypercube $\{0,1\}^n$. We work with multi-output circuits $C : \{0,1\}^n \to \{0,1\}^m$ where m > n and m is called the stretch of the circuit. A partial output assignment, $y \in \{0,1,*\}^m$, is a fixing of a subset of the output bits of the circuit to constants. For an input $x \in \{0,1\}^n$ to the circuit, we say x is consistent with a partial output assignment $y \in \{0,1,*\}^m$, if C(x) agrees with y on the fixed bits. When specified, the input (resp. output) space of a circuit might instead be viewed as the vector space \mathbb{F}_2^n (resp. \mathbb{F}_2^m) over the finite field \mathbb{F}_2 .

The complexity classes $\mathsf{FP}, \mathsf{FP}^{\mathsf{NP}}, \mathsf{FE}$, and $\mathsf{FE}^{\mathsf{NP}}$ are classes of search problems analogous to the classes of decision problems $\mathsf{P}, \mathsf{P}^{\mathsf{NP}}, \mathsf{E}$, and E^{NP} . For example, the class FP contains all functions that can be computed by deterministic polynomial-time Turing machines.

2.1 Circuits and Matrix Rigidity

In this paper, we work with circuit classes NC_k^0 and NC^1 , which we define below.

Definition 2.1. (NC Circuits) The circuit class NC^i contains multi-output Boolean circuits on n inputs of depth $O(\log^i(n))$ where each gate has fan-in 2. We are particularly concerned with the following classes of circuits:

- For every constant $k \ge 1$, NC_k^0 is the class of circuits where each output depends on at most k inputs.
- NC^1 is the class of circuits of depth $O(\log(n))$ where all gates have fan-in 2.
- Linear NC^1 circuits are circuits of depth $O(\log(n))$ where every gate has fan-in 2 and computes an affine function, i.e., the XOR of its two inputs or its negation.

It is a long-standing open problem in circuit complexity to prove super-linear lower bounds on the size of (linear) NC^1 circuits computing an *n*-output function from FP or even FE^{NP} [Val77, AB09, Frontier 3]. Valiant [Val77] suggested an approach for proving super-linear lower bounds for linear NC^1 circuits using the notion of matrix rigidity.

Definition 2.2 (Matrix Rigidity). For $r, s \in \mathbb{Z}^+$, a matrix $M \in \mathbb{F}_2^{n \times n}$ is (r, s)-rigid if M cannot be written as a sum

$$M = L + S \,,$$

where $L, S \in \mathbb{F}_2^{n \times n}$, L is low rank, i.e., rank $(L) \leq r$, and S is row sparse, i.e., every row of S has at most s non-zero entries.

Valiant [Val77] proved that a linear operator given by a sufficiently rigid matrix requires linear NC^1 circuits of size at least $\Omega(n \log \log(n))$, but there are still no known constructions of such rigid matrices even in FE^{NP} .

Theorem 2.3 ([Val77]). If a family of matrices $(M_n)_{n\geq 1}$, $M_n \in \mathbb{F}^{n\times n}$, is $(\varepsilon n, n^{\delta})$ -rigid for constant $\varepsilon, \delta > 0$, then the linear map $x \mapsto Mx$ requires linear NC¹ circuits of size $\Omega(n \log \log(n))$.

2.2 Range Avoidance for Circuits

In the range avoidance problem, given a circuit C with n inputs and m outputs, m > n, the goal is to find an m-bit string outside the range of C.

Definition 2.4 (AVOID). In the AVOID problem, given a description of a circuit $C : \{0,1\}^n \to \{0,1\}^m$ for m > n, the task is to find a $y \in \{0,1\}^m$ such that $\forall x \in \{0,1\}^n : C(x) \neq y$.

The function m = m(n) is called the *stretch* of the multi-output circuit C. Note that AVOID is a total search problem, i.e., there always exists such a $y \in \{0,1\}^m$ since m > n. We focus on a more restricted problem where there is an additional promise that the input circuit C is from a fixed circuit class C.

Definition 2.5 (C-AVOID). In the C-AVOID problem, given a description of a circuit $C : \{0, 1\}^n \to \{0, 1\}^m$ for m > n, where $C \in C$, the task is to find a $y \in \{0, 1\}^m$ such that $\forall x \in \{0, 1\}^n : C(x) \neq y$.

In particular, we are concerned with NC¹-AVOID and NC⁰_k-AVOID for constant $k \ge 1$. We will also consider the class of functions where each output is a multivariate polynomial of the inputs of degree at most d over \mathbb{F}_2 .

Definition 2.6 (degree-*d*-AVOID). In the *degree-d*-AVOID problem, given a description of a function C: $\mathbb{F}_2^n \to \mathbb{F}_2^m$ for m > n, where each output can be computed by a polynomial of degree $\leq d$ in the *n* inputs, the task is to find a $y \in \mathbb{F}_2^m$ such that $\forall x \in \mathbb{F}_2^n : C(x) \neq y$.

2.3 Low Degree and Low Locality

Perfect randomized encodings were introduced by [AIK06] for various cryptographic applications. We are interested in the following property of perfect encodings: For a Boolean function $f: \{0,1\}^n \to \{0,1\}^m$ and its encoding $\widehat{f}: \{0,1\}^{n+\ell} \to \{0,1\}^{m+\ell}$, there exists a polynomial-time decoding algorithm, Dec: $\{0,1\}^{m+\ell} \to \{0,1\}^m$, such that for all $y \in \{0,1\}^{m+\ell}$ and $x \in \{0,1\}^n$ satisfying Dec(y) = f(x), there exists $r \in \{0,1\}^\ell$ such that $y = \widehat{f}(x,r)$. This property can be used in AVOID reductions as follows. Given a solution to the AVOID problem for the function \widehat{f} , i.e., $y \notin \operatorname{Range}(\widehat{f})$, one can find a solution to the AVOID problem for the function \widehat{f} in polynomial time by simply computing Dec $(y) \notin \operatorname{Range}(f)$. [AIK06] first encode NC^1 functions as degree-3 functions. Then, they encode every degree-*d* function as an NC_{d+1}^0 function. Composing these two encodings provides an encoding of NC^1 functions in NC_4^0 . Using this encoding, [RSW22] provides a polynomial time reduction from NC^1 -AVOID to NC_4^0 -AVOID. We use only one part of the result from [AIK06]: there is a polynomial time reduction from degree-*d*-AVOID to NC_{d+1}^0 -AVOID. For completeness, we include the proof here.

Lemma 2.7. Let $d \geq 2$ be a constant, and $f: \mathbb{F}_2^n \to \mathbb{F}_2^m$ be a multi-output function where every output computes a sum of k monomials of degree $\leq d$. Then there exists a function $\widehat{f}: \{0,1\}^{n+(2k-1)m} \to \{0,1\}^{2km}$ computed by an NC_{d+1}^0 circuit and a polynomial time algorithm Dec: $\{0,1\}^{2km} \to \{0,1\}^m$ such that for all x, y, if $\mathsf{Dec}(y) = f(x)$, there exists an $r \in \{0,1\}^{(2k-1)m}$ such that $\widehat{f}(x,r) = y$.

Proof. We follow the encoding constructed in [AIK06]. First, we construct an encoding \hat{g} for each single output function g of f. Let $g(x) = T_1(x) + T_2(x) + \cdots + T_k(x)$ be a single output degree-d function where each $T_i(x)$ is a monomial of degree at most d. Consider the encoding of g, $\hat{g}: \{0,1\}^n \times \{0,1\}^{k} \times \{0,1\}^{k-1} \to \{0,1\}^{2k}$ defined as follows

$$\widehat{g}(x,r,s) = (T_1(x) - r_1, \quad T_2(x) - r_2, \quad \dots \quad T_{k-1}(x) - r_{k-1}(x), \quad T_k(x) - r_k, \\ r_1 - s_1, \quad s_1 + r_2 - s_2, \quad \dots \quad s_{k-2} + r_{k-1} - s_{k-1}, \quad s_{k-1} + r_k).$$

Clearly, each output bit of \hat{g} can be computed by an NC_{d+1}^0 circuit. We define a polynomial-time algorithm $\mathrm{Dec}_{\hat{g}} \colon \{0,1\}^{2k} \to \{0,1\}$ such that if $\mathrm{Dec}_{\hat{g}}(y) = g(x)$ then there exist r and s satisfying $\hat{g}(x,r,s) = y$. Given $y \in \{0,1\}^{2k}$, $\mathrm{Dec}_{\hat{g}}(y)$ sums up the bits of y modulo 2.

Suppose $\text{Dec}_{\widehat{q}}(y) = g(x)$ for some $x \in \{0, 1\}^n$, i.e.,

$$\operatorname{Dec}_{\widehat{g}}(y) = \sum_{j=1}^{2k} y_j = g(x) = \sum_{j=1}^k T_j(x) \,. \tag{1}$$

We will now show that there exist r and s such that $\widehat{g}(x,r,s) = y$. For each $j \in [k]$, we set $r_j = T_j(x) - y_j$. We also set $s_1 = r_1 - y_{k+1}$ and sequentially set $s_j = s_{j-1} + r_j - y_{k+j}$ for each $j \in \{2, \ldots, k-1\}$. By definition, the first 2k - 1 bits of $\widehat{g}(x,r,s)$ equal the first 2k - 1 bits of y. For the last bit, note that:

$$s_{k-1} + r_k = \sum_{i=1}^k r_i - \sum_{i=k+1}^{2k-1} y_i \qquad \text{(by the definition of } s)$$
$$= \sum_{i=1}^k T_i(x) - \sum_{i=1}^{2k-1} y_i \qquad \text{(by the definition of } r)$$
$$= y_{2k}. \qquad \text{(by Equation (1))}$$

Therefore, for the constructed r and s, $\hat{g}(x, r, s) = y$, as required.

Suppose $f(x) = (f_1(x), f_2(x), \dots, f_m(x))$, where each $f_i(x)$ is a sum of at most k monomials of degree $\leq d$. Let $\hat{f_i}$ be the encoding of f_i as defined above. Then our encoding of f is simply a concatenation of the encodings of its individual outputs, $\hat{f}: \{0,1\}^{n+(2k-1)m} \to \{0,1\}^{2km}$, where

$$\widehat{f}(x, r^{(1)}, r^{(2)}, \dots, r^{(m)}, s^{(1)}, s^{(2)}, \dots, s^{(m)}) = (\widehat{f}_1(x, r^{(1)}, s^{(1)}), \dots, \widehat{f}_m(x, r^{(m)}, s^{(m)})).$$
(2)

On input $y = (y_1, y_2, \dots, y_m) \in \{0, 1\}^{2km}$, the decoding algorithm returns

$$\operatorname{Dec}(y) = \left(\operatorname{Dec}_{\widehat{f_1}}(y_1), \dots, \operatorname{Dec}_{\widehat{f_m}}(y_m)\right).$$
(3)

Suppose Dec(y) = f(x) for some $x \in \{0,1\}^n$. Then $\text{Dec}_{\widehat{f}_i}(y_i) = f_i(x)$ for all $i \in [m]$. By our proof above, there exists $r^{(i)}$ and $s^{(i)}$ such that $y_i = \widehat{f}_i(x, r^{(i)}, s^{(i)})$ and, thereby,

$$y = (y_1, \dots, y_m) = (\widehat{f}_1(x, r^{(1)}, s^{(1)}), \dots, \widehat{f}_m(x, r^{(m)}, s^{(m)})) = \widehat{f}(x, r^{(1)}, s^{(1)}, \dots, r^{(m)}, s^{(m)}).$$

Finally, Dec runs in time O(mk) since it runs m iterations of $\text{Dec}_{\hat{f}_i}$ for each i, each of which simply computes a sum of 2k bits. Since each \hat{f}_i is in NC_{d+1}^0 , so is \hat{f} .

Now, following [RSW22], we conclude that there is a polynomial-time reduction from degree-d-AVOID to NC⁰_{d+1}-AVOID.

Corollary 2.8. For every $d \ge 1$, if there exists an FP (resp. FP^{NP}) algorithm for NC⁰_{d+1}-AVOID, then there exists an FP (resp. FP^{NP}) algorithm for degree-d-AVOID.

Proof. Let f be an input to a degree-d-AvOID problem with m output bits. Then, each output bit of f is a sum of at most $k = O(n^d)$ monomials of degree d. Let \hat{f} be the encoding of f in NC^0_{d+1} guaranteed by Lemma 2.7. Note that $\hat{f}: \{0,1\}^{n+(2k-1)m} \to \{0,1\}^{2km}$. By the assumption of the Corollary, there is an FP (resp. $\mathsf{FP}^{\mathsf{NP}}$) algorithm that returns a $y \notin \operatorname{Range}(\hat{f})$. Then, by Lemma 2.7, $\operatorname{Dec}(y) \notin \operatorname{Range}(f)$ and Dec runs in polynomial time. Therefore, there is an FP (resp. $\mathsf{FP}^{\mathsf{NP}}$) algorithm for degree-d-AvOID.

3 Hardness of NC_3^0 -Avoid

In this section, we reduce the problem of constructing explicit rigid matrices to the algorithmic task of solving NC_3^0 -AVOID. First, in Lemma 3.1 we give an explicit degree-2 function $f: \{0, 1\}^k \rightarrow \{0, 1\}^n$, $k \ll n$, whose range contains all sparse vectors of length n. Note that such a function f must have degree at least 2. Indeed, if f was affine and its range contained all vectors of sparsity at most 1, then its range must have dimension n, and the number of inputs of f would be $k \ge n$.

Next, in Theorem 3.2, we apply this lemma, together with the reduction from degree-2-AVOID to NC_3^0 -AVOID from Lemma 2.7, to conclude that an efficient algorithm for NC_3^0 -AVOID would provide an explicit construction of rigid matrices.

Lemma 3.1. For every $d \ge 1$ and every polynomial-time computable $s := s(n) < \frac{n^{1-1/d}}{d}$, there exists a polynomial-time computable function $f : \mathbb{F}_2^{dsn^{1/d}} \to \mathbb{F}_2^n$ whose range contains all vectors of sparsity at most s, and each output of f is a degree-d polynomial.

Proof. Let G be an arbitrary d-uniform hypergraph on $\ell = dn^{1/d}$ vertices and n hyperedges (such a graph exists because $\binom{\ell}{d} \geq (\ell/d)^d = n$). Fix an ordering $\{1, \ldots, \ell\}$ of the vertices and $\{1, \ldots, n\}$ of the edges. Each vertex of G will be labeled by a vector from \mathbb{F}_2^s . Our function $f: \mathbb{F}_2^{s\ell} \to \mathbb{F}_2^n$ will take as input the labels of the vertices of G and output n elements corresponding to the n hyperedges of G: the *i*th output is the generalized inner product of the labels of the d vertices in the *i*th hyperedge. We interpret the input as a matrix $X \in \mathbb{F}_2^{s \times \ell}$, where the *j*th column $X_j \in \mathbb{F}_2^s$ is the label corresponding to the *j*th vertex. Suppose the hyperedge *i* contains the vertices $\{j_1, \ldots, j_d\}$

then the *i*th output is $f_i(X) = \sum_{k=1}^s X_{k,j_1} \cdots X_{k,j_d}$. Clearly, f is a degree-d function, it only remains to show that its output contains all vectors of sparsity $\leq s$. For this, we show that for every vector $y \in \mathbb{F}_2^n$ of sparsity $\leq s$, there is an input, i.e., a labeling of the vertices of G, such that f outputs y. Let the s non-zero elements of y correspond to the distinct edges i_1, \ldots, i_s in G. For each vertex j in G we set its label $X_j \in \mathbb{F}_2^s$ to be such that $(X_j)_k = 1$ if $j \in i_k$ and $(X_j)_k = 0$ otherwise.

Consider any edge $i = \{j_1, \ldots, j_d\}$ and the submatrix X_{j_1, \ldots, j_d} of X containing the labels of these vertices connected by i.

- If $i = i_k$ for some $k \in [s]$, then $y_i = 1$. The i_k th row of X_{j_1,\ldots,j_d} contains all 1 entries. Furthermore, every other row contains at least one zero. Therefore, $f_i(X) = 1$.
- If $i \neq i_k$ for all $k \in [s]$, then $y_i = 0$ and each row of X_{j_1,\ldots,j_d} contains at least one zero. Therefore, $f_i(X) = 0$.

Equipped with Lemma 3.1, we are ready to show that an efficient algorithm for degree-2-AVOID or NC_3^0 -AVOID would imply an explicit construction of rigid matrices.

Theorem 3.2. For every constant $1/2 \leq \delta \leq 1$, an FP (resp. FP^{NP}) algorithm for degree-2-AVOID with stretch $m = 2n^{2/(1+\delta)}$ will provide an FP (resp. FP^{NP}) algorithm for finding an $(n^{\delta}/10, n^{\delta-1/2}/10)$ -rigid matrix.

Furthermore, for every $1/2 \le \delta \le 1$, an FP (resp. FP^{NP}) algorithm for NC₃⁰-Avoid with stretch $m = n + O(n^{2/(2+\delta)})$ will provide an FP (resp. FP^{NP}) algorithm for finding an $(n^{\delta}/10, n^{\delta-1/2}/10)$ -rigid matrix.

Proof. Let $r = n^{\delta}/10$ and $s = n^{\delta-1/2}/10$. First, we reduce (in deterministic polynomial time) the problem of finding an (r, s)-rigid matrix to solving degree-2-Avoid for a function $g: \mathbb{F}_2^{4rn} \to \mathbb{F}_2^{n^2}$.

Suppose $M \in \mathbb{F}_2^{n \times n}$ is not (r, s)-rigid. Then, M can be written as a sum M = Q + S, where rank $(Q) \leq r$ and S is s-row sparse. Furthermore, $Q = L \cdot R$ for some matrices $L, R^T \in \mathbb{F}_2^{n \times r}$.

We view the input of g as 2rn entries of the matrices L and R, and $2sn^{3/2}$ inputs of n copies of the degree-2 function f from Lemma 3.1 needed to encode the entries of n sparse rows of S. Then the function g simply outputs all n^2 entries of $M = L \cdot R + S$. Note that each output of g computes a dot-product of a row of L and a column of R, and adds a degree-2 output of f. Therefore, we constructed a degree-2 function g whose range contains all non-rigid matrices. A solution to degree-2-Avoid on input g would therefore give an (r, s)-rigid matrix. The number of inputs of g is $n' = 2rn + 2sn^{3/2} = 4rn = 2n^{1+\delta}/5$, and the stretch of the function g is at least $m'(n') \geq 2(n')^{2/(1+\delta)}$. This concludes the proof of the first part of the theorem.

For the second part, we use the polynomial-time reduction from degree-2-AVOID to NC_3^0 -AVOID from Lemma 2.7. By the construction above, we have a degree-2 function $g: \mathbb{F}_2^{4rn} \to \mathbb{F}_2^{n^2}$ where each output bit is the sum of at most $t = r + s \leq 2r$ degree-2 monomials. We apply Lemma 2.7 to reduce AVOID for g to AVOID for an NC_3^0 function $\hat{g}: \{0,1\}^{\hat{n}} \to \{0,1\}^{\hat{m}}$, where $\hat{n} = n' + (2t-1)n^2$ and $\hat{m} = 2tn^2$. This yields a stretch of $\hat{m}(\hat{n}) = \hat{n} + O(\hat{n}^{2/(2+\delta)})$ for the function \hat{g} . Therefore, an algorithm for NC_3^0 -AVOID for stretch $\hat{m}(n)$ yields an (r, s)-rigid matrix.

We remark that in the regime $\delta > 1/2$, Theorem 3.2 would give matrices that for rank n^{δ} have higher rigidity than all known constructions of rigid matrices in FP, FP^{NP} and FE^{NP}. Therefore, for every $\varepsilon > 0$, an FP^{NP} algorithm for degree-2-AVOID with stretch $n^{12/11-\varepsilon}$ or an FP^{NP} algorithm for NC₃⁰-AVOID with stretch $n + n^{12/17+\varepsilon}$ would lead to new rigidity lower bounds. Since the regime of $\delta = 1$ in Theorem 3.2 is sufficient for Valiant's program of proving super-linear lower bounds on the size of linear NC¹ circuits (see Theorem 2.3), we have the following corollary.

Corollary 3.3. An FP (resp. $\mathsf{FP}^{\mathsf{NP}}$) algorithm for degree-2-AVOID with stretch m = 2n or for NC_3^0 -AVOID with stretch $m = n + O(n^{2/3})$ will provide a linear function in FP (resp. $\mathsf{FP}^{\mathsf{NP}}$) that cannot be computed by linear NC^1 circuits of size $o(n \log \log(n))$.

4 Algorithms for NC_k^0 -Avoid

In this section, we describe polynomial-time algorithms for solving NC_k^0 -AVOID with non-trivial stretch. More specifically, we provide an algorithm that runs in time $2^{O(n^{k-1}/m)} \cdot \operatorname{poly}(n)$ when the stretch of the input circuit is at least $m \ge \Omega(n^{k-2})$. First, we describe a useful structural property of NC_k^0 circuits, which follows from the following simple graph-theoretic result.

Lemma 4.1. For any constants $c \ge 1$ and $k \ge 3$, every k-uniform hypergraph G = (V, E) with n vertices and $m \ge cn^{k-2}$ hyperedges contains a subset of vertices $V' \subseteq V, |V'| \le cn^{k-1}/m$ and a subset of hyperedges $E' \subseteq E, |E'| \ge cn$ such that each hyperedge in E' contains at least k - 2 vertices from V'. Furthermore, there is a polynomial-time algorithm that finds such a V' and E'.

Proof. Consider the following bipartite graph H with vertex set $A \sqcup B$ where $A = \{u_S \mid S \subseteq V, |S| = k - 2\}$ is the set of vertices indexed by the k - 2 sized subsets of V and $B = \{v_e \mid e \in E\}$ is indexed by the edges of G. Furthermore there is an edge (u_S, v_e) in H if $S \subseteq e$, i.e., if all the vertices in S are contained in the hyperedge $e \in E$. Since each hyperedge e contains k vertices, the degree of each vertex in B is exactly $\binom{k}{k-2}$. Then, the average degree of the vertices in A and let N(A') $\frac{|B|\binom{k}{k-2}}{|A|} = \frac{m\binom{k}{k-2}}{\binom{n}{k-2}}$. Let $A' \subseteq A$ be the subset of t vertices with highest degree in A and let N(A')

be their neighbors in *B*. Then total degree of vertices in *A'* is at least $\frac{tm\binom{k}{k-2}}{\binom{n}{k-2}}$. Since each vertex in *B* has degree $\binom{k}{k-2}$, $|N(A')| \ge \frac{tm\binom{k}{k-2}}{\binom{n}{k-2}\binom{k}{k-2}} = \frac{tm}{\binom{n}{k-2}}$. Therefore, setting $t = \frac{cn\binom{n}{k-2}}{m}$, *V'* to be the set of $t(k-2)\binom{n}{k-2} \le cm\frac{k-1}{k-2}$ we retice of *C* contained in the union of the vertex subsets

set of $t(k-2) = \frac{cn(k-2)\binom{n}{k-2}}{m} \leq cn^{k-1}/m$ vertices of G contained in the union of the vertex subsets in A', and E' = N(A') completes our proof.

To find V' and E', we first construct the graph H which has polynomial size, and then find the vertices in A' by finding the t vertices in A with maximum degree. It is now straightforward to construct V' from A' and to find E' = N(A').

Corollary 4.2. For any constants $c \ge 1$ and $k \ge 3$, given an NC_k^0 circuit C with n inputs and $m \ge cn^{k-2}$ outputs, there exists a subset of outputs O of size $|O| \ge cn$, and a subset of inputs I of size $|I| \le cn^{k-1}/m$, such that for every output bit $C_i \in O$, at least k - 2 of the input bits feeding into C_i are from I. Furthermore, there is a polynomial-time algorithm that finds such sets I and O.

Proof. Without loss of generality we assume that each output of C reads exactly k inputs (as if it reads $\ell < k$ inputs, we let it additionally read arbitrary $k - \ell$ inputs and ignore them). Consider the hypergraph where each vertex corresponds to one of the n inputs $\{x_1, \ldots, x_n\}$ of C. Each edge of the hypergraph corresponds to an output C_i , $e_i = \{j \mid C_i \text{ reads } x_j\}$. Now, we apply Lemma 4.1 on this hypergraph and set I = V' and O = E'. Note that $|I| = |V'| \leq cn^{k-1}/m$ and $|O| = |E'| \geq cn$. \Box

This corollary finds a linear number of output bits O of the circuit that mostly depend on a small number of common input bits I. Our algorithm for NC_k^0 -AVOID will "branch" on all possible assignments to the inputs from I. Each such assignment will correspond to an affine subspace $\mathcal{S} \subseteq \mathbb{F}_{2}^{n}$ of the input space. Then, our algorithm works by fixing the output bits from O such that the sum of the dimensions of these affine subspaces is significantly reduced at each step, eventually making all subspaces empty. Note that by the guarantee of Corollary 4.2, after fixing the inputs I, each output from O depends on at most two inputs. Thus, we need an efficient way to reduce the dimension of the affine subspace containing the consistent inputs for the case where output functions depend on at most two inputs. In Lemma 4.3, we provide such a subroutine AFFINEREDUCE (Algorithm 1).

Lemma 4.3. Let $S \subseteq \mathbb{F}_2^n$ be an affine subspace, and $f: \mathbb{F}_2^n \to \mathbb{F}_2$ be a function that depends on at most two inputs. The algorithm AFFINEREDUCE in deterministic polynomial time finds two affine subspaces (or empty sets) $S_0, S_1 \subseteq S$ such that

- (1) $\forall x \in \mathcal{S}, b \in \mathbb{F}_2$, if f(x) = b, then $x \in \mathcal{S}_b$;
- (2) $|S_0| + |S_1| \le 3|S|/2.$

Proof. Without loss of generality we assume that f(x) depends on (a subset of) x_1 and x_2 . We will consider three cases depending on the degree of f, and in each case we will find affine subspaces (or empty sets) $\mathcal{S}_0, \mathcal{S}_1 \subseteq \mathcal{S}$ such that at least one of them has dimension strictly smaller than the dimension of \mathcal{S} (or at least one of them is an empty set). This will ensure that $|\mathcal{S}_0| + |\mathcal{S}_1| \leq 3|\mathcal{S}|/2$.

- If f(x) = c for some $c \in \mathbb{F}_2$ is a constant function, then we set $\mathcal{S}_c = \mathcal{S}$ and $\mathcal{S}_{1-c} = \emptyset$. Clearly, $S_c = S$ and S_{1-c} contain all points $x \in S$ that are consistent with f(x) = c and f(x) = 1 - c, respectively.
- If $f(x) = a_1x_1 + a_2x_2 + c$ for some constants $a_1, a_2, c \in \mathbb{F}_2$ is an affine function, then for each $b \in \mathbb{F}_2$ let H_b be the hyperplane defined by $a_1x_1 + a_2x_2 + c = b$, and let $\mathcal{S}_b = \mathcal{S} \cap H_b$. Again, $S \cap H_b$ contains all the inputs in S that are consistent with f(x) = b. Furthermore, if $\dim(\mathcal{S}_0) = \dim(\mathcal{S})$, then $\mathcal{S} \subseteq H_0$ and $\mathcal{S}_1 = \mathcal{S} \cap H_1 = \emptyset$. Therefore, either $\dim(\mathcal{S}_0) < \dim(\mathcal{S})$ or $\mathcal{S}_1 = \emptyset$.
- If $f(x) = (x_1 + a_1)(x_2 + a_2) + c$ for some constants $a_1, a_2, c \in \mathbb{F}_2$ is a quadratic function, then let \mathcal{H} be the affine subspace defined by $\mathcal{H} = \{x \in \mathbb{F}_2^n \mid x_1 = 1 + a_1, x_2 = 1 + a_2\}$. Consider the affine subspace $S_{1-c} = S \cap H$ which contains all points $x \in S$ satisfying f(x) = 1 - c.
 - If $\dim(\mathcal{S}_{1-c}) < \dim(\mathcal{S})$, then we are done as we can take $\mathcal{S}_c = S$
 - If $\dim(\mathcal{S}_{1-c}) = \dim(\mathcal{S})$, then $\mathcal{S} \subseteq \mathcal{H}$. Then, every point in \mathcal{S} satisfies f(x) = 1 c, thus, setting $S_{1-c} = S$ and $S_c = \emptyset$ completes our construction.

In each case, either $|\mathcal{S}_0| \leq \frac{|\mathcal{S}|}{2}$ or $|\mathcal{S}_1| \leq \frac{|\mathcal{S}|}{2}$. Therefore, $|\mathcal{S}_0| + |\mathcal{S}_1| \leq 3|\mathcal{S}|/2$. The only computation made by AFFINEREDUCE is to compute the dimensions of explicitly given affine subspaces, which can be performed in polynomial time.

A simple application of AFFINEREDUCE recovers a polynomial-time algorithm for NC_2^0 -Avoid from [GLW22].

 $\begin{array}{l} \textbf{Algorithm 1 AFFINEREDUCE}(\mathcal{S},f) \\ \hline \textbf{Input: Affine subspace } \mathcal{S} \subseteq \mathbb{F}_2^n, f \colon \mathbb{F}_2^n \to \mathbb{F}_2 \text{ that may depend only on } x_1 \text{ and } x_2 \\ \textbf{Output: } \mathcal{S}_0, \mathcal{S}_1 \subseteq \mathcal{S} \\ \textbf{if } f(x) = c \textbf{ then} \\ \textbf{return } \mathcal{S}_c = \mathcal{S} \text{ and } \mathcal{S}_{1-c} = \emptyset \\ \textbf{if } f(x) = a_1 x_1 + a_2 x_2 + c \textbf{ then} \\ \text{For } b \in \mathbb{F}_2, \text{ let } H_b = \{x \in \mathbb{F}_2^n \colon a_1 x_1 + a_2 x_2 + c = b\} \\ \textbf{return } \mathcal{S}_0 = \mathcal{S} \cap H_0 \text{ and } \mathcal{S}_1 = \mathcal{S} \cap H_1 \\ \textbf{if } f(x) = (x_1 + a_1)(x_2 + a_2) + c \textbf{ then} \\ \text{Let } \mathcal{H} = \{x \in \mathbb{F}_2^n \colon x_1 = 1 + a_1, x_2 = 1 + a_2\} \\ \text{Let } \mathcal{S}_{1-c} = \mathcal{S} \cap \mathcal{H} \\ \textbf{if } \dim(\mathcal{S}_{1-c}) < \dim(\mathcal{S}) \textbf{ then} \\ \textbf{return } \mathcal{S}_{1-c} \text{ and } \mathcal{S}_c = \mathcal{S} \\ \textbf{else} \\ \textbf{return } \mathcal{S}_{1-c} \text{ and } \mathcal{S}_c = \emptyset \end{array}$

Corollary 4.4. There is a deterministic polynomial-time algorithm that, given an NC_2^0 circuit $C: \{0,1\}^n \to \{0,1\}^m, m \ge n+1$, finds an element $y \in \{0,1\}^m, y \notin Range(C)$.

Proof. At iteration $1 \leq i \leq n+1$, our algorithm will fix the value of the *i*th output bit y_i . The algorithm also maintains an affine subspace $S \subseteq \mathbb{F}_2^n$ that contains all inputs $x \in \mathbb{F}_2^n$ consistent with the partial output assignments of y_1, \ldots, y_i . By Lemma 4.3, there exists an assignment $y_i = b$, such that either none of the inputs in S_b are consistent with y or the dimension of $S = S_b$ reduces at least by one. In the former case, we already find our desired output y (we can just set the unassigned bits of y to arbitrary values). Otherwise, after fixing the first n outputs, we have dim(S) = 0, i.e., $S = \{x\}$ for some $x \in \mathbb{F}_2^n$. Let $b \in \{0, 1\}$ be the value of the (n + 1)th output bit of C(x). Then setting $y_{n+1} = 1 - b$ produces our desired output y. This algorithm runs in polynomial time since it makes at most n calls to AFFINEREDUCE and one call to C(x).

Finally, equipped with Lemma 4.3, we are ready to present our main algorithm for NC_k^0 -AVOID.

Theorem 4.5. Given an NC_k^0 circuit $\mathsf{C}: \{0,1\}^n \to \{0,1\}^m$, where $m \ge 3n^{k-2}$, the algorithm SUB-SPACEUNION finds an element $y \in \{0,1\}^m$, $y \notin \operatorname{Range}(\mathsf{C})$ in deterministic time $2^{O(n^{k-1}/m)} \cdot \operatorname{poly}(n)$.

Proof. First we apply Corollary 4.2 with c = 3 to the circuit C, and select in polynomial time a subset of inputs $I = \{x_1, \ldots, x_t\}$ and a set of outputs $O = \{y_1, y_2, \ldots, y_{3n}\}$ for $t \leq 3n^{k-1}/m$. This ensures that each y_i has at most two inputs outside of I. For each of the 2^t assignments of the inputs from I, we consider a circuit where the values of these t inputs are fixed. Namely, for $j \in \{0, \ldots, 2^t - 1\}$, we fix the inputs in I to the bits in the binary representation of j. Then we restrict the circuit C to the outputs y_1, \ldots, y_{3n} and all the inputs that feed them, and obtain a circuit C_j , where each output depends on at most 2 inputs. We'll find a value $y \in \{0, 1\}^{3n}$ that no C_j outputs, and this will give us a solution to the original NC_k^0 -Avoid instance.

Our algorithm will maintain the following invariant. At the *i*th iteration of the algorithm after we fix the values of the outputs y_1, \ldots, y_i , we maintain $\mathcal{U} = \bigcup_{j=0}^{2^t-1} \mathcal{U}_j$, a disjoint union of 2^t affine subspaces, such that all inputs $x \in \mathbb{F}_2^n$ that are consistent with y_1, \ldots, y_i belong to \mathcal{U} (and \mathcal{U} may contain points that are inconsistent with y_1, \ldots, y_i , too). In the beginning of the algorithm, for every $0 \leq j < 2^t$, we let \mathcal{U}_j be the affine subspace where the inputs in I are fixed to the bits in the binary representation of j. Then $\mathcal{U} = \bigcup_j \mathcal{U}_j = \mathbb{F}_2^n$ is the set of all inputs consistent with our initial empty partial assignment.

At every step *i*, we will show how to find a constant $b \in \{0, 1\}$ such that after fixing $y_i = b$, the size of our disjoint union $|\mathcal{U}|$ reduces by a factor of 4/3. Therefore, after repeating this procedure for the 3*n* outputs from *O*, we will have an empty \mathcal{U} , and the constructed partial assignment will give us a solution to the NC⁰_k-AvOID problem.

At the *i*th iteration of the algorithm, we have values of outputs y_1, \ldots, y_{i-1} fixed, and are to fix the value of y_i . We have two choices: either set $y_i = 0$ or set $y_i = 1$. By Lemma 4.3, we have two affine subspaces (or empty sets) $\mathcal{U}_{j,0}, \mathcal{U}_{j,1} \subseteq \mathcal{U}_j$ containing all inputs $x \in \mathcal{U}_j$ mapping to $y_i = 0$ and $y_i = 1$, respectively. Moreover, Lemma 4.3 guarantees that $|\mathcal{U}_{j,0}| + |\mathcal{U}_{j,1}| \leq 3|\mathcal{U}_j|/2$. Summing over all $0 \leq j < 2^t$, we get

$$\sum_{j} |\mathcal{U}_{j,0}| + \sum_{j} |\mathcal{U}_{j,1}| \leq \sum_{j} 3|\mathcal{U}_{j}|/2 = 3|\mathcal{U}|/2.$$

Let $b \in \{0, 1\}$ be the value minimizing $\sum_{j} |\mathcal{U}_{j,b}|$. In particular, we have that $\sum_{j} |\mathcal{U}_{j,b}| \leq 3|\mathcal{U}|/4$. Therefore, setting $y_i = b$ reduces the size of \mathcal{U} at least by a factor of 4/3. Repeating this procedure $\log_{4/3}(2^n) + 1 \leq 3n$ times will result in a partial assignment to the output bits O with no inputs that map to it.

The algorithm SUBSPACEUNION maintains 2^t affine subspaces of \mathbb{F}_2^n , computes their dimensions and calls the deterministic polynomial-time AFFINEREDUCE procedure polynomial number of times. Therefore, this algorithm runs in time $2^t \cdot \operatorname{poly}(n) = 2^{O(n^{k-1}/m)} \cdot \operatorname{poly}(n)$.

Algorithm 2 SUBSPACEUNION(C)

```
Input: \mathsf{NC}_k^0 circuit \mathsf{C} : \{0,1\}^n \to \{0,1\}^m, where m \ge 3n^{k-2}

Output: y \in \{0,1\}^m, y \notin \operatorname{Range}(\mathsf{C})

Find x_1, \ldots, x_t and y_1, \ldots, y_{3n} via Corollary 4.2 for t \le 3n^{k-1}/m

For 0 \le j < 2^t, set \mathcal{U}_j = \{x \in \{0,1\}^n : \sum_{i=1}^t x_i 2^{i-1} = j\}

for i=1 to 3n do

Find function f at y_i

For 0 \le j < 2^t, set \mathcal{U}_{j,0}, \mathcal{U}_{j,1} \leftarrow \operatorname{AFFINEREDUCE}(\mathcal{U}_j, f)

Find b \in \{0,1\} minimizing \sum_j |\mathcal{U}_{j,b}|

Set y_i = b

For 0 \le j < 2^t, set \mathcal{U}_j = \mathcal{U}_{j,b}

Set all remaining y_k = 0

return y
```

We conclude this section with a corollary stating that SUBSPACEUNION solves NC_k^0 -AVOID efficiently for certain non-trivial values of stretch m.

Corollary 4.6. For any constants $k \geq 3$ and $\varepsilon > 0$, the algorithm SUBSPACEUNION solves NC_k^0 -AVOID on n inputs and m outputs in deterministic polynomial and deterministic sub-exponential $2^{O(n^{1-\varepsilon})}$ time for $m \geq n^{k-1}/\log(n)$ and $m \geq n^{k-2+\varepsilon}$, respectively.

Acknowledgements

We thank Justin Thaler, Sam King, and anonymous reviewers for their helpful comments on our paper.

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A An Alternative Algorithm for NC_3^0 -Avoid

Theorem A.1. Given an NC_3^0 circuit $C : \{0,1\}^n \to \{0,1\}^m$, where $m \ge \frac{1}{3}\binom{n}{2} + 2n$, the algorithm ONESUBSPACE finds an element $y \in \{0,1\}^m$, $y \notin \text{Range}(C)$ in deterministic polynomial time.

Proof. The algorithm maintains an affine subspace $S \subseteq \mathbb{F}_2^n$ over the inputs, and a partial output assignment $y \in \{0, 1, *\}^m$ such that S contains all inputs $x \in \mathbb{F}_2^n$ consistent with y. Initially, $y = (*, \ldots, *)$ and $S = \mathbb{F}_2^n$. At each iteration, ONESUBSPACE assigns at most two outputs and reduces the dimension of S by at least 1. After n steps, S must have dimension 0. Then the algorithm assigns one more output bit, and terminates with an element $y \notin \text{Range}(\mathsf{C})$.

Now, we only need to argue that the algorithm can reduce the dimension of S in each iteration and that we can perform each step in polynomial time.

First, if there is an output y_1 that depends on at most 2 inputs x_1, x_2 , let f be the function computed at that output: $y_1 = f(x_1, x_2)$. By Lemma 4.3, AFFINEREDUCE(S, f) outputs an affine subspace S_b of lower dimension dim(S_b) $< \dim(S)$, containing all inputs consistent with $y_1 = b$. Thus, in the following we assume that each output depends on exactly 3 inputs.

Since we fix at most 2 bits of the output at each iteration, the number of unassigned outputs m is always greater than $\frac{1}{3} \binom{n}{2}$. Then, there exists a pair of outputs y_1, y_2 that both depend on the same pair of inputs x_2, x_3 . Since each output depends on three pairs of inputs, and the number of such pairs is $\binom{n}{2} < 3m$, there must be a pair of inputs that feeds into two outputs. Let x_1, x_4 be the remaining inputs that feed into the outputs y_1, y_2 , respectively: $y_1 = f_1(x_1, x_2, x_3), y_2 = f_2(x_2, x_3, x_4)$.

There exist 4 possible values of (y_1, y_2) and at most 16 possible values of the input bits (x_1, x_2, x_3, x_4) appearing in S. Then, there exists a pair of constants $(b_1, b_2) \in \{0, 1\}^2$ such that at most 4 different assignments $A \subseteq \{0, 1\}^4$ to (x_1, x_2, x_3, x_4) are consistent with the partial assignment $(y_1, y_2) = (b_1, b_2)$. Since $|A| \leq 4$, there is a 3-dimensional affine subspace in \mathbb{F}_2^4 that contains all points from A. Therefore, there is a hyperplane $\mathcal{H} \subseteq \mathbb{F}_2^4$ defining this 3-dimensional affine subspace. Extending \mathcal{H} to all n inputs, gives us an affine subspace $\mathcal{H}' = \{x \in \mathbb{F}_2^n : (x_1, x_2, x_3, x_4) \in \mathcal{H}\} \subseteq \mathbb{F}_2^n$ that contains all inputs in \mathbb{F}_2^n consistent with the partial assignment $(y_1, y_2) = (b_1, b_2)$.

If $\mathcal{S} \not\subseteq \mathcal{H}'$, then setting $(y_1, y_2) = (b_1, b_2), \mathcal{S} = \mathcal{S} \cap \mathcal{H}'$ reduces the dimension of the affine subspace \mathcal{S} . In the following we assume that $\mathcal{S} \subseteq \mathcal{H}'$.

- Suppose there exists $(c_1, c_2) \in \{0, 1\}^2$ such that for all points (x_1, x_2, x_3, x_4) in \mathcal{H}' , $(f_1(x_1, x_2, x_3), f_2(x_2, x_3, x_4)) \neq (c_1, c_2)$. Then we can set $(y_1, y_2) = (c_1, c_2)$ and $\mathcal{S} = \emptyset$ as no points in $\mathcal{S} \subseteq \mathcal{H}'$ can output (c_1, c_2) . In this case we found a $y \notin \text{Range}(\mathsf{C})$.
- If there are no such assignments, then since $|\mathcal{H}| = 8$, there must exist an assignment $(c_1, c_2) \in \{0, 1\}^2$ such that at most two points from \mathcal{H} are consistent with $(y_1, y_2) = (c_1, c_2)$. These (at most) two points form a 0- or 1-dimensional affine subspace $\mathcal{U} \subseteq \mathbb{F}_2^4$, which we extend to all n inputs $\mathcal{U}' = \{x \in \mathbb{F}_2^n : (x_1, x_2, x_3, x_4) \in \mathcal{U}\} \subseteq \mathbb{F}_2^n$.
 - If $\mathcal{S} \not\subseteq \mathcal{U}'$, we can set $(y_1, y_2) = (c_1, c_2)$ and $\mathcal{S} = \mathcal{S} \cap \mathcal{U}'$, reducing the dimension of \mathcal{S} .
 - Otherwise, all inputs in $S \subseteq U'$ have $(y_1, y_2) = (c_1, c_2)$, and we can set $(y_1, y_2) = (1 c_1, c_2)$ to obtain $S = \emptyset$.

This algorithm performs n iterations, each of which computes dimensions of a constant number of explicitly given affine subspaces in polynomial time.

Algorithm 3 ONESUBSPACE(C) **Input:** NC₃⁰ circuit C : $\{0,1\}^n \to \{0,1\}^m$, where $m \ge \frac{1}{3} {n \choose 2} + 2n$ **Output:** $y \in \{0,1\}^m$, $y \notin \text{Range}(\mathsf{C})$ Let $\mathcal{S} = \mathbb{F}_2^n$ for i=1 to n do if $S = \emptyset$ then Set all remaining $y_k = 0$ return yif $\exists y_1, x_1, x_2$ s.t. $y_1 = f(x_1, x_2)$ then $\mathcal{S}_0, \mathcal{S}_1 = \text{AFFINEREDUCE}(\mathcal{S}, f)$ Find $b \in \{0, 1\}$ that minimizes $|S_b|$, Set $y_1 = b$, $S = S_b$ else Find $y_1, y_2, x_1, x_2, x_3, x_4$ s.t. $y_1 = f_1(x_1, x_2, x_3), y_2 = f_2(x_2, x_3, x_4)$ Find $b_1, b_2 \in \{0, 1\}$, s.t. $A = \{(x_1, x_2, x_3, x_4) \in \mathbb{F}_2^4 : (f_1(x_1, x_2, x_3), f_2(x_2, x_3, x_4)) = (b_1, b_2)\} \text{ and } |A| \le 4$ Let $\mathcal{H} \subseteq \mathbb{F}_2^4$ be the hyperplane defined by points in A Let $\mathcal{H}' = \{x \in \mathbb{F}_2^n \colon (x_1, x_2, x_3, x_4) \in \mathcal{H}\}$ if $\mathcal{S} \nsubseteq \mathcal{H}$ then Set $(y_1, y_2) = (b_1, b_2), \mathcal{S} = \mathcal{S} \cap \mathcal{H}'$ else if $\exists (c_1, c_2)$ s.t. $\forall (x_1, x_2, x_3, x_4) \in \mathcal{H} (f_1(x_1, x_2, x_3), f_2(x_2, x_3, x_4)) \neq (c_1, c_2)$ then Set $S = \emptyset$, $(y_1, y_2) = (c_1, c_2)$ else Find (c_1, c_2) s.t. $\mathcal{U} = \{(x_1, x_2, x_3, x_4) \in \mathcal{H} \colon (f_1(x_1, x_2, x_3), f_2(x_1, x_2, x_3)) = (c_1, c_2)\}, |\mathcal{U}| \le 2$ Let $\mathcal{U}' = \{x \in \mathbb{F}_2^n | (x_1, x_2, x_3, x_4) \in \mathcal{U}\}$ if $\mathcal{S} \nsubseteq \mathcal{U}'$ then Set $\mathcal{S} = S \cap \mathcal{U}', (y_1, y_2) = (c_1, c_2)$ else $S = \emptyset, (y_1, y_2) = (1 - c_1, c_2)$ return y

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